

Amendments to the Specification:

The paragraph on page 12, line 1, has been rewritten as shown:

Aspects for reducing jitter in a PLL of a high speed serial link ~~are described.~~ The aspects include examining at least one parameter related to performance of a voltage controlled oscillator (VCO) in the PLL, and controlling adjustment of a supply voltage to the VCO based on the examining. A regulator control circuit performs the examining and controlling.